

Memory access

Vital for performance!

Memory types

Coalescing

Example of using shared memory



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Memory types

Global

Shared

Constant (read only)

Texture cache (read only)

Local

Registers

Care about these when optimizing - not to begin with



Global memory

400-600 cycles latency!

Shared memory fast temporary storage

Coalesce memory access!

Continuous
Aligned on power of 2 boundary
Addressing follows thread numbering

Use shared memory for reorganizing data for coalescing!



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Using shared memory to reduce number of global memory accesses

Read blocks of data to shared memory

Process

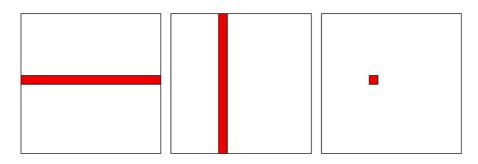
Write back as needed

Shared memory as "manual cache"

Example: Matrix multiplication



Matrix multiplication



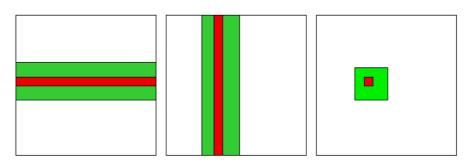
To multiply two N*N matrices, every item will have to be accessed N times!

Naive implementation: 2N3 global memory accesses!



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Matrix multiplication



Let each block handle a part of the output.

Load the parts of the matrix needed for the block into shared memory.



Matrix multiplication on CPU

Simple triple "for" loop

```
void MatrixMultCPU(float *a, float *b, float *c, int theSize)
{
   int sum, i, j, k;

   // For every destination element
   for(i = 0; i < theSize; i++)
        for(j = 0; j < theSize; j++)
        {
        sum = 0;
        // Sum along a row in a and a column in b
        for(k = 0; k < theSize; k++)
            sum = sum + (a[i*theSize + k]*b[k*theSize + j]);
        c[i*theSize + j] = sum;
    }
}</pre>
```



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Naive GPU version

Replace outer loops by thread indices

```
__global__ void MatrixMultNaive(float *a, float *b, float *c, int
theSize)
{
    int sum, i, j, k;
    i = blockIdx.x * blockDim.x + threadIdx.x;
    j = blockIdx.y * blockDim.y + threadIdx.y;

    // For every destination element
    sum = 0;
    // Sum along a row in a and a column in b
    for(k = 0; k < theSize; k++)
        sum = sum + (a[i*theSize + k]*b[k*theSize + j]);
    c[i*theSize + j] = sum;
}</pre>
```



Naive GPU version inefficient

Every thread makes 2N global memory accesses!

Can be significantly reduced using shared memory



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Optimized GPU version

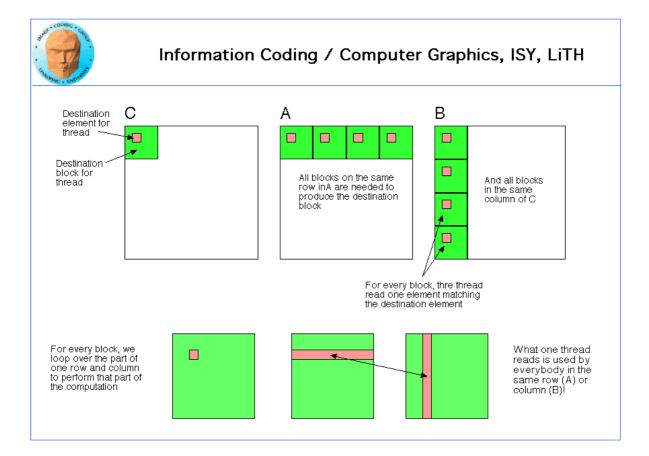
Data split into blocks.

Every element takes part in all the blocks in the same *row* for A, *column* for B

For every such block

Every thread reads *one* element to shared memory

Then loop over the appropriate row and column for the block





Optimized GPU version

Loop over blocks (1D)

Allocate shared memory

Copy one element to shared memory

Loop over row/column in block, compute, accumulate result for one element

Write result to global memory



Modified computing model:

Upload data to global GPU memory

For a number of parts, do:

Upload partial data to shared memory

Process partial data

Write partial data to global memory

Download result to host



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Synchronization

As soon as you do something where one part of a computation depends on a result from another thread, you must synchronize!

__syncthreads()

Typical implementation:

- · Read to shared memory
- __syncthreads()
- Process shared memory
- synchthreads()
- Write result to global memory



Summary:

- · Make threads and blocks to make the hardware occupied
 - · Access data depending on thread/block number
 - Memory accesses are expensive!
 - Shared memory is fast
 - · Make threads within a block cooperate
 - Synchronize



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That's all folks!

Next: More about memory management and optimization.